

AMENDMENT UNDER 37 C.F.R. § 1.111  
U.S. APPLICATION NO. 09/975,961  
ATTORNEY DOCKET NO. Q66404

**Please delete the present Abstract of the Disclosure, and add the following new  
Abstract of the Disclosure:**

A data packet switching node, for use in an asynchronous digital network, that has an input stage that cuts data packets into segments of constant length, a switching matrix having input ports and output ports supporting identical bit rates  $B$  switching the segments and an output stage reconstructing the data packets from the segments supplied by the output ports of said switching matrix. The input stage has at least one input interface with a bit rate equal to a multiple of  $B$ ,  $k_i * B$ , and splits the data packet into  $k_i$  input ports of the switching matrix. The output stage has at least one output interface with a bit rate equal to a multiple of  $B$ ,  $k_o * B$ , and reconstructs a data packet with a bit rate equal to  $k_o * B$  by concatenating segments supplied by  $k_o$  output ports of the switching matrix where  $k_i * k_o > 1$ .